

### Features

- Formerly a KEKOVARICON product
- Two standard model sizes available -7.5 x 9.0 mm and 8 x 12 mm (smaller sizes available upon request)
- Through-hole and surface mount styles
- Operating voltage range (Vdc): 16, 20, 26, 38 and 56 V
- Capacitance range: 0.47 pF to 1.5 µF (higher values available upon request)
- Available in tape and reel packaging for automatic pick-and-place
- AEC-Q200 Grade 1 upon request
- RoHS compliant\*

## **OV Series** – Automotive Grade Dual Function Varicons

### **General Information**

The OV series is a series of dual function protective devices that help protect against voltage surges in a voltage region and against radio frequency noise. This component typically replaces two components – a low voltage varistor and a capacitor.

OV series varicons incorporate a varistor function in a voltage region (12 V, 24 V, 42 V) and a function of a radio-frequency filtering capacitor in a high capacitance range from 0.47 to 1.5  $\mu$ F (higher values are available upon request), making them ideal for protection in certain automobile electronics applications.

OV varicons are square shaped components with in-line leads, which require very little mounting space - at least 30 % less than the two components they typically replace. Dual function varicons are also available in an SMD version upon request and are compliant with Pb-free soldering.

### **Absolute Maximum Ratings**

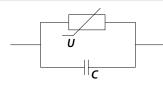
Parameter	Value	Units
Continuous:		
Steady State Applied Voltage		
DC Voltage Range (V <sub>dc</sub> )	16 to 56	V
AC Voltage Range (V <sub>rms</sub> )	14 to 40	V
<b>Transient:</b> Load Dump Energy (WLD) Jump Start Capability - 5 minutes (V <sub>jump</sub> ) Non-Repetitive Surge Current, 8/20 μs Waveform (I <sub>max</sub> )	6 to 12 24 to 65 800 to 1200	J V A
Non-Repetitive Surge Energy, 10/1000 µs Waveform (Wmax)	2.4 to 10.5	J
Capacitance Range	470 to 4700	nF
Capacitor Temperature Characteristics	X7R	
Operating Ambient Temperature	-40 to +125	°C
Storage Temperature Range	-40 to +150	°C
Threshold Voltage Temperature Coefficient	< +0.05	%/°C
Insulation Resistance	> 1	GΩ
Isolation Voltage Capability	> 1.25	kV
Response Time	< 5	ns
Climatic Category	40 / 125 / 56	

#### **Additional Information**

Click these links for more information:



### **Dual Function Component Symbol**



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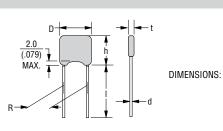
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### **Device Ratings**

	V <sub>rms</sub>	V <sub>dc</sub>	Vn	Vjump	v <sub>c</sub>	I <sub>c</sub>	W <sub>max</sub>	WLD	Р	I <sub>max</sub>	С Тур.
Model	-1115		@ 1 mA	5 min.	-0		10/1000 µs	10 times	max.	8/20 μs	@ 1 kHz
	V	V	V	V	V	А	J	V	W	А	μF
12 V Supply Voltage											
OV 14 K 474 MX 801	14	16	24	24.5	40	5	2.4	6	0.015	800	0.47
OV 14 K 105 MX 801	14	16	24	24.5	40	5	2.4	6	0.015	800	1.00
OV 14 K 155 MX 801	14	16	24	24.5	40	5	2.4	6	0.015	800	1.50
OV 14 K 474 MX 122	14	16	24	24.5	40	10	5.8	12	0.030	1200	0.47
OV 14 K 105 MX 122	14	16	24	24.5	40	10	5.8	12	0.030	1200	1.00
OV 14 K 155 MX 122	14	16	24	24.5	40	10	5.8	12	0.030	1200	1.50
OV 17 K 474 MX 801	17	20	27	30	44	5	2.8	6	0.015	800	0.47
OV 17 K 105 MX 801	17	20	27	30	44	5	2.8	6	0.015	800	1.00
OV 17 K 155 MX 801	17	20	27	30	44	5	2.8	6	0.015	800	1.50
OV 17 K 474 MX 122	17	20	27	30	44	10	7.4	12	0.030	1200	0.47
OV 17 K 105 MX 122	17	20	27	30	44	10	7.4	12	0.030	1200	1.00
OV 17 K 155 MX 122	17	20	27	30	44	10	7.4	12	0.030	1200	1.50
24 V Supply Voltage											
OV 20 K 474 MX 801	20	26	33	36	54	5	3.2	6	0.015	800	0.47
OV 20 K 105 MX 801	20	26	33	36	54	5	3.2	6	0.015	800	1.00
OV 20 K 155 MX 801	20	26	33	36	54	5	3.2	6	0.015	800	1.50
OV 20 K 474 MX 122	20	26	33	36	54	10	7.8	12	0.030	1200	0.47
OV 20 K 105 MX 122	20	26	33	36	54	10	7.8	12	0.030	1200	1.00
OV 20 K 155 MX 122	20	26	33	36	54	10	7.8	12	0.030	1200	1.50
OV 30 K 474 MX 801	30	38	47	50	77	5	4.5	6	0.015	800	0.47
OV 30 K 105 MX 801	30	38	47	50	77	5	4.5	6	0.015	800	1.00
OV 30 K 155 MX 801	30	38	47	50	77	5	4.5	6	0.015	800	1.50
OV 30 K 474 MX 122	30	38	47	50	77	10	10	12	0.030	1200	0.47
OV 30 K 105 MX 122	30	38	47	50	77	10	10	12	0.030	1200	1.00
OV 30 K 155 MX 122	30	38	47	50	77	10	10	12	0.030	1200	1.50
42 V Supply Voltage		1			1						1
OV 40 K 474 MX 801	40	56	68	65	110	5	4.8	6	0.015	800	0.47
OV 40 K 105 MX 801	40	56	68	65	110	5	4.8	6	0.015	800	1.00
OV 40 K 155 MX 801	40	56	68	65	110	5	4.8	6	0.015	800	1.50
OV 40 K 474 MX 122	40	56	68	65	110	10	10.5	12	0.030	1200	0.47
OV 40 K 105 MX 122	40	56	68	65	110	10	10.5	12	0.030	1200	1.00
OV 40 K 155 MX 122	40	56	68	65	110	10	10.5	12	0.030	1200	1.50

"X" indicates X7R temperature characteristics; other capacitance values are available upon request.

#### **Product Dimensions**



Surge Current Code	D max.	h max.	R	d	t max.
801	7.5	<u>9.0</u>	<u>5.0</u>	<u>0.6</u>	<u>5.5</u>
	(.295)	(.354)	(.197)	(.024)	(.217)
122	<u>8.0</u>	<u>12.0</u>	<u>5.0</u>	<u>0.6</u>	<u>5.5</u>
	(.315)	(.472)	(.197)	(.024)	(.217)

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How to Order

MM

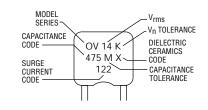
(INCHES)

#### OV20K474MX801RL1yy Series Designator **OV** Series Max. Continuous Working Voltage (V<sub>rms</sub>) Vn Tolerance $K = \pm 10 \%$ Capacitance Code • 474 = 470 nF • 105 = 1 µF • 155 = 1.5 μF Capacitance Tolerance $M = \pm 20 \%$ Dielectric Type X = X7RSurge Current Code 801 = 800 A 122 = 1200 A Packaging -B = Bulk R = Reel A = Ammo Pack (available upon request) Lead Style 1 = Straight (Leaded Component Only) Special Parameters

# Instructions for Creating Orderable Part Number:

- 1) Start with base part number in characteristics table (example: <u>OV20K474MX801</u>).
- 2) Add Packaging: R (example part number becomes OV20K474MX801<u>R</u>).
- 3) Add Lead Style: L1 (example part number becomes OV20K474MX801RL1)
- 4) Part number can have no spaces or lower case letters.

#### **Typical Part Marking**



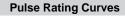
Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

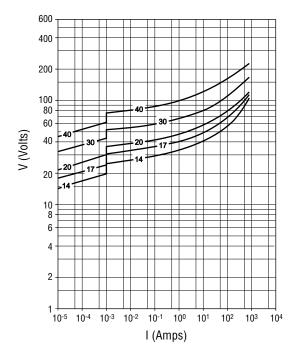
### BOURNS

### **Protection Level**

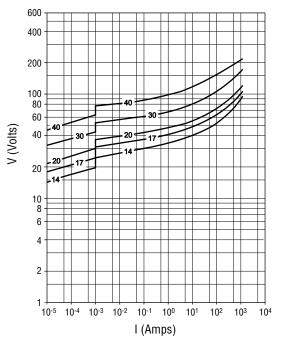
### Surge Current Code 801 - (OV14 ~ OV40)

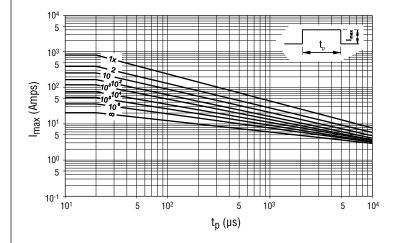


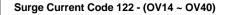
Surge Current Code 801 - (OV14 ~ OV40)

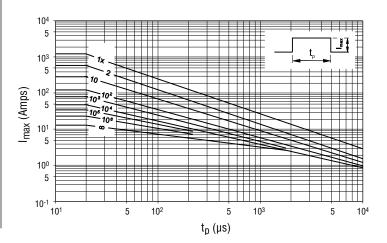


### Surge Current Code 122 - (OV14 ~ OV40)





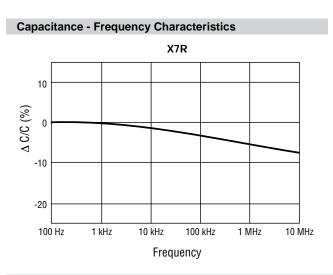


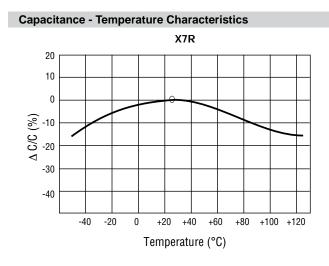


Specifications are subject to change without notice.

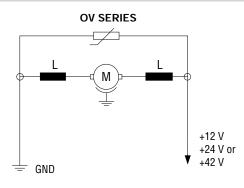
Users should verify actual device performance in their specific applications.

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**Application Circuit** 



Provides for the elimination of transients and EMI noise in automotive electronics such as engine control, exhaust gas control, safety systems, etc., against disturbances caused by small motors used in automobiles. Most frequently, small motors in an automobile are those used for windscreen wipers, window mechanisms, seat adjustments and automatic door locking.

Specifications are subject to change without notice.

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Users should verify actual device performance in their specific applications.
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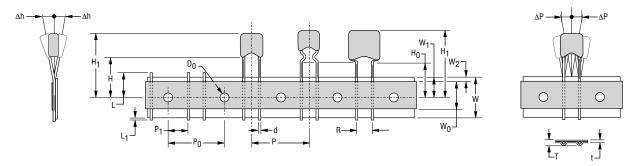
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### **Packaging Specifications**

#### Таре

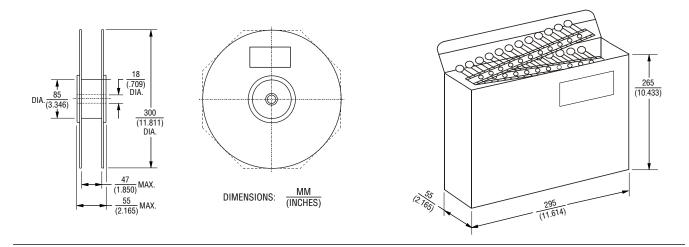
Reel

Conforms to IES Publication 286-2 Ed. 3: 2008-03



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#### Ammo Pack (Available upon Special Request)



Specifications are subject to change without notice.

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Symbol	Parameter	Dimension
W	Carrier tape width	<u>18 +1.0/-0.5</u> (.709 +.039/020)
W <sub>0</sub>	Hold down tape width	<u>5</u> (.197) MIN.
W <sub>1</sub>	Sprocket hole position	<u>9 +0.75/-0.5</u> (.354 +.030/020)
W <sub>2</sub>	Distance between the upper edges of the carrier tape and hold down tape	3 (.118) MAX.
Т	Total tape thickness	1.5 (.059) MAX.
t	Tape thickness	<u>0.9</u> (.035) MAX.
Р	Pitch of component	$\frac{12.7 \pm 1.0}{(.500 \pm .039)}$
P <sub>0</sub>	Feed hole pitch	$\frac{12.7 \pm 0.3}{(.500 \pm .012)}$
P <sub>1</sub>	Feed hole center to pitch	$\frac{3.85 \pm 0.7}{(.152 \pm .028)}$
R	Lead spacing	<u>5 +0.5/-0.2</u> (.197 +.020/008)
ΔΡ	Component alignment	$\frac{\pm 1.3}{(\pm .051)}$ MAX.
Δh	Component alignment	±2 (±.079) MAX.
d	Wire diameter	<u>0.6</u> (.024) MAX.
D <sub>0</sub>	Feed hold diameter	$\frac{4 \pm 0.2}{(.157 \pm .008)}$
н	Height from tape center to component base	<u>18 +2.0/-0.0</u> (.709 +.079/000)
H <sub>0</sub>	Seating plane height	$\frac{16 \pm 0.5}{(.630 \pm .020)}$
H <sub>1</sub>	Component height	32.2 (1.268) MAX.
L	Protrusion - cut out	11 (.433) MAX.
L <sub>1</sub>	Protrusion - cut off	<u>0.5</u> (.020) MAX.

### Packaging Specifications - Tape (Continued)

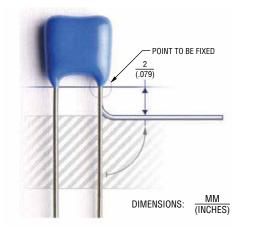
MM DIMENSIONS: (INCHES)

### **Packaging Quantities**

Bulk	1000
Reel	1000
Ammo Pack	1000

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#### Assembly Recommendations for Through-Hole Components



Very often before soldering through-hole components, their leads get bent. It is important not to damage the components during lead bending. Damage most commonly incurred during bending is cracks in epoxy parts, which can lead to increased humidity sensitivity of a component and, consequentially, a shorter lifetime.

In order to avoid epoxy damage, it is necessary to:

- fix the most sensitive point (epoxy parts) of a component body
- bend the wire at least 2 mm below the end of epoxy parts

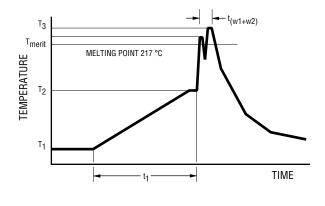
Other potential damage to a component which can lead to component failure or a shorter lifetime is thermal shock during manual soldering with a soldering iron. This can occur when a soldering iron is placed too close to one point of the component body and it happens most often when the solder joint is too close to the varistor body.

#### **Resistance to Soldering Heat**

In the case of automatic wave soldering, it is important to provide sufficient resistance to soldering heat. In order to prevent any potential problems, internal standards were introduced for testing the resistance to soldering heat of through-hole components: 300 °C, 10 seconds.

#### **Pb-free Wave Soldering Profile Recommendations**

Recommended soldering profiles for all above components are in accordance with JEDEC standard curves (J-STD-020D) and are, therefore, compatible with the Pb-free process.



Lead-free Wave Soldering Profile - Pb-free	wave profile requirements for	r soldering heat resistance of components
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Parameter	Symbol	Specification
Preheating temperature gradient		4 °C/sec. max.
Preheating time	t <sub>1</sub>	2 to 5 min.
Min. preheating temperature	T <sub>1</sub>	130 °C
Max. preheating temperature	T <sub>2</sub>	180 °C
Melting temperature/point	T <sub>meltv</sub>	217 °C
Time in wave soldering phase (w <sub>1</sub> +w <sub>2</sub> )	<sup>t</sup> w1+w2	10 sec.
Max. wave temperature (w1+w2)	Тs	265 °C +0/-5 °C
Cooling temperature gradient		6° C/sec. max.
Temperature jump from $T_2$ to $T_3$ (w <sub>1</sub> )	T <sub>3(w1)</sub> - T <sub>2</sub>	120 °C max
Time from 25 °C to T <sub>3</sub> (wave temperature)		8 min. max.

Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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### **Reliability Testing Procedures**

Varistor test procedures comply with CECC 42200, IEC 1051-1/2 (and AEC-Q200, if applicable). Test results are available upon customer request. Special tests can be performed upon customer request.

Reliability Parameter	Test	Tested According to	Condition to be Satisfied after Testing
AC/DC Bias Reliability	AC/DC Life Test	CECC 42200, Test 4.20 or IEC 1051-1, Test 4.20, AEC-Q200 Test 8 - 1000 h at UCT	lδV <sub>n</sub> (1 mA)l < 10 %
Pulse Current Capability	I <sub>max</sub> 8/20 μs	CECC 42200, Test C 2.1 or IEC 1051-1, Test 4.5 10 pulses in the same direction at 2 pulses per minute at maximum peak current for 10 pulses	lδV <sub>n</sub> (1 mA)l < 10 % no visible damage
Pulse Energy Capability	W <sub>max</sub> 10/1000 µs	CECC 42200, Test C 2.1 or IEC 1051-1, Test 4.5 10 pulses in the same direction at 1 pulse every 2 minutes at maximum peak current for 10 pulses	lδV <sub>n</sub> (1 mA)l < 10 % no visible damage
WLD Capability	WLD x 10	ISO 7637, Test pulse 5, 10 pulses at rate of 1 per minute	$ \delta V_{n} (1 \text{ mA})  < 15 \%$ no visible damage
V <sub>jump</sub> Capability	V <sub>jump</sub> 5 min.	Increase of supply voltage to $V \ge V_{jump}$ for 1 minute	$ \delta V_n (1 \text{ mA})  < 15 \%$ no visible damage
Environmental and Storage Reliability	Climatic Sequence	CECC 42200, Test 4.16 or IEC 1051-1, Test 4.17 a) Dry heat, 16h, UCT, Test Ba, IEC 68-2-2 b) Damp heat, cyclic, the first cycle: 55 °C, 93 % RH, 24 h, Test Db 68-2-4 c) Cold, LCT, 2 h, Test Aa, IEC 68-2-1 d) Damp heat cyclic, remaining 5 cycles: 55 °C, 93 % RH, 24 h/cycle, Test Bd, IEC 68-2-30	lδV <sub>n</sub> (1 mA)l < 10 %
Storage Kenability	Thermal Shock	CECC 42200, Test 4.12, Test Na, IEC 68-2-14, AEC-Q200 Test 16, 5	$ \delta V_{\rm fl} (1 \text{ mA})  < 10 \%$ no visible damage
	Steady State Damp Heat	CECC 42200, Test 4.17, Test Ca, IEC 68-2-3, AEC-Q200 Test 6, 56 days, 40 °C, 93 % RH, AEC-Q200 Test 7: Bias, Rh, T all at 85.	ΙδV <sub>n</sub> (1 mA)l < 10 %
	Storage Test	IEC 68-2-2, Test Ba, AEC-Q200 Test 3, 1000 h at maximum storage temperature	lδV <sub>n</sub> (1 mA)l < 5 %

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Specifications are subject to change without notice.

## BOURNS

### **Reliability Testing Procedures (Continued)**

Reliability Parameter	Test	Tested According to	Condition to be Satisfied after Testing
	Solderability	CECC 42200, Test 4.10.1, Test Ta, IEC 68-2-20 solder bath and reflow method	Solderable at shipment and after 2 years of storage, criteria: >95% must be covered by solder for reflow meniscus
	Resistance to Soldering Heat	CECC 42200, Test 4.10.2, Test Tb, IEC 68-2-20 solder bath nad reflow method	lδV <sub>n</sub> (1 mA)l < 5 %
	Terminal Strength	JIS-C-6429, App. 1, 18N for 60 sec same for AEC-Q200 Test 22	No visual damage
Mechanical Reliability	Board Flex	JIS-C-6429, App. 2, 2 mm min. AEC-Q200 test 21 - Board flex: 2 mm flex min.	lδV <sub>n</sub> (1 mA)l < 2 % No visible damage
	Vibration	CECC 42200, Test 4.15, Test Fc, IEC 68-2-6, AEC-Q200 Test 14 Frequency range 10 to 55 Hz (AEC: 10-2000 Hz) Amplitude 0.75 m/s <sup>2</sup> or 98 m/s <sup>2</sup> (AEC: 5 g for 20 minutes) To- tal duration 6 h (3x2 h) (AEC: 12 cycles each of 3 directions) Waveshape - half sine	lδV <sub>n</sub> (1 mA)l < 2 % No visible damage
	Mechanical Shock	CECC 42200, Test 4.14, Test Ea, IEC 68-2-27, AEC-Q200 Test 13. Acceleration = 490 m/s <sup>2</sup> (AEC: MIL-STD-202-Method 213), Pulse duration = 11 ms, Waveshape - half sine; Number of shocks = $3x6$	lδV <sub>n</sub> (1 mA)l < 10 % No visible damage
Electrical Transient Conduction	ISO-7637-1 Pulses	AEC-Q200 Test 30: Test pulses 1 to 3. Also other pulses - freestyle.	lδV <sub>n</sub> (1 mA)l < 10 % No visible damage

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### Terminology

Term	Symbol	Definition
		Maximum continuous sinusoidal AC voltage (<5 % total harmonic distortion) which may be applied to the component under continuous operation conditions at +25 °C
		Maximum continuous DC voltage (<5 % ripple) which may be applied to the component under continuous operating conditions at +25 °C
		The voltage by which the system is designated and to which certain operating characteristics of the system are referred; $V_{rms} = 1.1 \text{ x V}$
Leakage Curreent	I <sub>dc</sub>	The current passing through the varistor at $V_{dc}$ and at +25 $^\circ\text{C}$ or at any other specified temperature
Varistor Voltage	V <sub>n</sub>	Voltage across the varistor measured at a given reference current $(I_n)$
Reference Current		
Protection Level	-	The peak voltage developed across the varistor under standard atmospheric conditions, when passing an 8/20 $\mu \rm s$ class current pulse
Class Current	I <sub>c</sub>	A peak value of current which is 1/10 of the maximum peak current for 100 pulses at two per minute for the 8/20 $\mu \rm s$ pulse
Voltage Clamping Ratio	V <sub>c</sub> /V <sub>app</sub>	A figure of merit measure of the varistor clamping effectiveness as defined by the symbols $V_c/V_{app}$ , where ( $V_{app} = V_{rms}$ or $V_{dc}$ )
Jump Start Transient	V <sub>jump</sub>	. The jump start transient results from the temporary application of an overvoltage in excess of the rated battery voltage. The circuit power supply may be subjected to a temporary overvoltage condition due to the voltage regulation failing or it may be deliberately generated when it becomes necessary to boost start the car.
Rated Single Pulse Transient Energy	W <sub>max</sub>	Energy which may be dissipated for a single $10/1000 \ \mu$ s pulse of a maximum rated current, with rated AC voltage or rated DC voltage also applied, without causing device failure
Load Dump Transient	WLD	Load Dump is a transient which occurs in automotive environments. It is an exponentially decaying positive voltage which occurs in the event of a battery disconnect while the alternator is still generating charging current with other loads remaining on the alternator circuit at the time of battery disconnect.
Rated Peak Single Pulse Transient Current	I <sub>max</sub>	Maximum peak current which may be applied for a single 8/20 $\mu \rm s$ pulse, with rated line voltage also applied, without causing device failure
Rated Transient Average Power Dissipation	Ρ	Maximum average power which may be dissipated due to a group of pulses occurring within a specified isolated time period, without causing device failure at 25 °C
Capacitance	C	Capacitance between two terminals of the varistor measured @ 1 kHz
Non-linearity Exponent	α	<ul> <li>A measure of varistor nonlinearity between two given operating currents, I<sub>n</sub> and I<sub>1</sub> as described by I = k V exp(a), where:</li> <li>- k is a device constant,</li> <li>- I<sub>1</sub> &lt; I &lt; I<sub>n</sub> and</li> <li>- a log (I<sub>1</sub>/I<sub>n</sub>)/log(V<sub>1</sub>/V<sub>n</sub>) = 1/log (V<sub>1</sub>/V<sub>n</sub>), where:</li> <li>- I<sub>r</sub> is reference current (1 mA) and V<sub>n</sub> is varistor voltage</li> <li>- I<sub>1</sub> = 10 I<sub>n</sub>, V<sub>1</sub> is the voltage measured at I<sub>1</sub></li> </ul>
		The time lag between application of a surge and varistor's "turn-on" conduction action
Varistor Voltage Temperature Coefficient	TC	(V <sub>n</sub> @ 85 °C - V <sub>n</sub> @ 25 °C) / (V <sub>n</sub> @ 25 °C) x 60 °C) x 100
Insulation Resistance	IR	Minimum resistance between shorted terminals and varistor surface
Isolation Voltage		The maximum peak voltage which may be applied under continuous operating conditions between the varistor terminations and any conducting mounting surface
Operating Temperature		The range of ambient temperature for which the varistor is designed to operate continuously as defined by the temperature limits of its climatic category
Climatic Category	LCT/UCT/DHD	LCT & UCT = Lower and Upper Category Temperature - the minimum and maximum ambient temperatures for which a varistor has been designed to operate continuously. DHD = Dump Heat Test Duration
Storage Temperature		Storage temperature range without voltage applied
Current/Energy Derating		Derating of maximum values when operated above UCT

### REV. B 09/20

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